

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Shannon *et al.*
Serial Number: 10/523,727
Filing Date: September 7, 2006
Confirmation Number: 1094
Examiner: DIAZ, Jose
Art Unit: 2815
Title: FIELD EFFECT TRANSISTOR

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO RESTRICTION REQUIREMENT

Dear Sir:

In response to the Restriction Requirement set forth in the Office action issued on December 24, 2008, Applicants elect, without traverse, **claims of Group I, i.e. claims 1-25** drawn to a semiconductor device.

An early and favorable action on the merits is respectfully requested.

Dated: 13-JAN-2009

Respectfully submitted,

By /Mark Beloborodov/
Mark L. Beloborodov, Esq., Registration No.: 50,773

Philips Intellectual Property & Standards
Three Burlington Woods Drive
Burlington, MA 01803
(781) 418-9363